

Review of: "Nanotubes other than CMOS nanotransistors in making gauges and actuators; Supercapacitors are also used in many other industries"

Malton Pereira¹

¹ Manchester College

Potential competing interests: No potential competing interests to declare.

The structure behaves as a metal conductor, while the chiral structure behaves as a semiconductor, and its reaction is a small part of the energy gap with carbon nanotubes, which have unique electrical and mechanical properties. For example, the structure of a nanotube is a thousand times more than that of copper, and the metal nanotube is capable of carrying an electric current with a density of cm/A. These characteristics have led to the use of this material in the manufacture of electronic devices such as CMOS (semiconductor) transistors. Carbon nanotubes have been proposed as a replacement for silicon in the MOSFET transistor channel. Nanotubes can solve some problems of reducing the length of the channel in the transistor, such as electron tunneling from the inside of the channel or from the gate to the inside of the channel.

Nanotubes are used in making gauges and actuators other than CMOS nanotransistors; supercapacitors are also used in many other industries. The main problem in the application of nanotubes is that they must be used lying on the surface in order to be able to bond them and establish a metallic connection to achieve CMOS transistor behavior. This is because the nanotubes grow vertically. In addition, there should be the possibility of precise control over the characteristics of each nanotube as well as its growth location and length. They will increase the speed of integrated electronics as much as possible. In nanoelectronic circuits, especially RF and microwave blocks, very high-speed switches are needed. Usually, the transistors with records of very high speeds are 2 and inhomogeneous bipolar MOSFET transistors and high electron mobility, up to about 600GHz and 750GHz.

Conclusion:

CMOS nano-transistors are combined with compound semiconductors, especially nano-tubes, the structure of nano-electronic compounds, and perhaps optical and opto-electronic devices can benefit the most from these semiconductor compounds. The main reason is the possibility of engineering the energy gap in these compounds, unlike silicon.

[1][2][3][4][5][6][7][8][9][10][11][12][13]

References

1. [^] Alex Atkinson. (2023). *Review of: "CMOS nanotransistors are combined with compound semiconductors, especially nanotubes"*. Qeios. doi:10.32388/09tdk9.
2. [^] Alex Atkinson. (2023). *Review of: "Linking nanostructures and nanotransistors"*. Qeios. doi:10.32388/yz3p5q.
3. [^] Monta O,konte. (2023). *Review of: "(linking nanoelectronics and nanoplasmonics) many advantages such as ease of production, the possibility of industrialization, the ability to control the dimensions of the raw materials of nanochips and nanotransistors"*. Qeios. doi:10.32388/r9g095.
4. [^] Anita Gupta. (2023). *Review of: "Amplification of Nano Wires Nano Wire by Electron Nano Lithography"*.Qeios. doi:10.32388/l3md1n.
5. [^] Afshin Rashid. (2023). *Review of: "Nano wire immersion method (structure and function)"*.Qeios. doi:10.32388/0od0gl.
6. [^] Afshin Rashid. (2023). *Review of: "(Field effect nano transistors) Nano transistor electronic quantity and ionization potential)"*. Qeios. doi:10.32388/464lg7.
7. [^] Lei Peng. (2023). *Review of: "Nano plasmonics and changes in the structure (nano transistors) Nano transistor"*. Qeios. doi:10.32388/bg7qxd.
8. [^] Afshin Rashid. (2023). *Review of: "The concept of (Nano assembler) in smart electronic nano structures"*.Qeios. doi:10.32388/atyte1.
9. [^] Andrea County. (2023). *Review of: "The concept of (Nano assembler)"*. Qeios. doi:10.32388/xrrt0e.
10. [^] Afshin Rashid. (2023). *Review of: "Propagation of Oligophenylene vanillin nanowires by focused ion beam (FIB) nanolithography method (below 1 · · nm - 1 · nm range)"*. Qeios. doi:10.32388/whhfa8.
11. [^] Criystian Orlando. (2023). *Review of: "nanowires by focused ion beam (FIB) nanolithography method"*. Qeios. doi:10.32388/vxmrt2.
12. [^] Afshin Rashid. (2023). *Review of: "Oligophenylene vanillin (silicon/germanium) structured nanowires and cylinders for possible applications in electronic energy"*. Qeios. doi:10.32388/i5wrmf.
13. [^] Martin Harisson. (2023). *Review of: "vanillin nanowires by focused ion beam (FIB) nanolithography method (below 1 · · nm - 1 · nm range)"*. Qeios. doi:10.32388/zhw4v2.